	Application No.	Applicant(s)
Notice of Allowability	09/987,039	TAKATA, TOSHIO
	Examiner	Art Unit
	C.DAS	2122
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	ears on the cover sheet with (OR REMAINS) CLOSED in or other appropriate communication is su	n the correspondence address this application. If not included nication will be mailed in due course. THIS
1. This communication is responsive to <u>11/13/01</u> .		
2. The allowed claim(s) is/are <u>1-14</u> .		
3. The drawings filed on 13 November 2001 are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b)		
 Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 6/16/04 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Sur Paper No./N 8), 7. ☐ Examiner's A	fail Date mendment/Comment statement of Reasons for Allowance

Application/Control Number: 09/987,039

Art Unit: 2122

1. Claims 1-14 have been allowed.

REASON FOR ALLOWANCE

2. The following is an examiner's statement of reason for allowance:

The cited prior art taken alone or in combination fail to teach, in combination with the other claimed limitations, a method of modifying a program stored in a nonvolatile memory in a microcontroller, specifically, the second program replacing the disabled part of the first program and an interrupt-handling routine for redirecting program execution from the disabled part of the first program to the second program, and the processing unit executes the first program responds to the interrupt signal by executing the interrupt-handling routine, is thereby redirected to the second program, executes the second program, and then returns to execution of the first program, as recited in the independent claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments of Statement of Reason for Allowance".

Conclusion

3. The prior art made or record and not relied upon is considered pertinent to applicant's disclosure.

TITLE: Method and apparatus for analyzing software in a language-independent manner, US 6658651 B2

TITLE: Software migration on an active processing element, US 6698017 B1

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TITLE: Method and apparatus for extending BIOS control of screen display beyond operating system boot process, US 6477642 B1

TITLE: Microprocessor with an architecture mode control capable of supporting extensions of two distinct instruction-set architectures, US 5854913 A

TITLE: Computer system with private and shared partitions in cache, US 5875464 A

TITLE: Multiprocessor system with write generate method for updating cache, US 5524212 A.

TITLE: The Reduction of Branch Instruction Execution Overhead Using Structured Control Flow, author: Wedig et al, IEEE, 1984.

TITLE: Instruction Fetch Mechanisms for Multipath Execution Processors, author: Klauser et al, IEEE, 1999.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Chameli Das whose telephone number is 703-305-1339.

The examiner can normally be reached on Monday-Friday from 7:00 A.M. to 3:30 P.M.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor Tuan Dam can be reached at 703-305-4552. The fax number for this group is (703) 872-9306.

An inquiry of general nature or relating to the status of this application or proceeding should be directed to the group receptionist whose telephone number is 703-305-9600.

CHAMELI C. DAS PRIMARY EXAMINER

6/16/04

Chamb-C-Dm